



PATENT APPLICATION  
Do. No. 9898-204

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jae-Yoon SIM and Jei-Hwan YOO

Serial No. 09/901,930

Examiner: Not Yet Assigned

Filed: July 9, 2001

Group Art Unit: 2204

For: NEGATIVE VOLTAGE GENERATOR FOR A SEMICONDUCTOR  
MEMORY DEVICE

Assistant Commissioner for Patents  
Washington, D.C. 20231

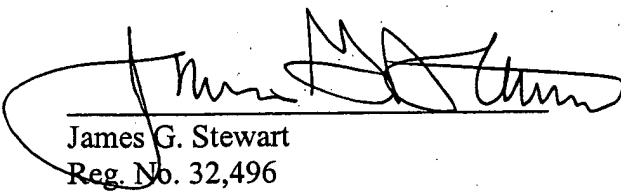
REQUEST FOR CORRECTION OF FILING RECEIPT

The filing receipt for the above-referenced matter does not show the priority information for this application (Exhibit A). Attached is a copy of a Preliminary Amendment (Exhibit B), filed September 17, 2001 for the above-identified application, which inserts the corresponding Provisional Application Serial Number 60/288,746, which was unavailable at the time of filing.

Applicant's Declaration (Exhibit C) for the above-identified application indicates that under Title 35, United States Code, Sec 119(e) the Applicant claims priority from Provisional Application entitled NEGATIVE VOLTAGE GENERATOR FOR A SEMICONDUCTOR MEMORY DEVICE, which was filed May 4, 2001.

Applicant requests correction of the Filing Receipt to indicate the priority based on Provisional Application No. 60/288,746 filed May 4, 2001.

Respectfully submitted,  
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/901,930	07/09/2001	2818	2204	9898-204	6	63	12

CONFIRMATION NO. 7100

## FILING RECEIPT



\*OC000000006478766\*

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Date Mailed: 08/27/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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Jei-Hwan Yoo, Kyunggi-do, KOREA, REPUBLIC OF;

## Assignment For Published Patent Application

Samsung Electronics Co., Ltd., Suwon-City, KOREA, REPUBLIC OF;

## Domestic Priority data as claimed by applicant

## Foreign Applications

If Required, Foreign Filing License Granted 08/27/2001

Projected Publication Date: 01/09/2003

Non-Publication Request: No

Early Publication Request: No

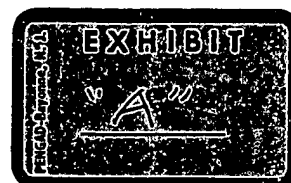
## Title

Negative voltage generator for a semiconductor memory device

## Preliminary Class

365

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**Data entry by : TRAN, DONG-CHAU**

**Team : OIPE**

**Date: 08/27/2001**

11/1/2001

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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